

DCR470E34

Phase Control Thyristor

DS6047-2 June 2019 (LN38867)

FEATURES

- Double Side Cooling
- High Surge Capability

APPLICATIONS

- High Power Drives
- High Voltage Power Supplies
- Static Switches

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{DRM} and V _{RRM} V	Conditions
DCR470E34 DCR470E32 DCR470E30 DCR470E28 DCR470E26 DCR470E24	3400 3200 3000 2800 2600 2400	$\begin{split} T_{vj} = -40^{\circ}\text{C to } 125^{\circ}\text{C}, \\ I_{DRM} = I_{RRM} = 30\text{mA}, \\ V_{DRM}, V_{RRM} t_p = 10\text{ms}, \\ V_{DSM} \& V_{RSM} = \\ V_{DRM} \& V_{RRM} +100V \\ respectively \end{split}$

Lower voltage grades available.

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DCR470E34

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

KEY PARAMETERS

3400 V
470 A
6300 A
1000 V/µs
150 A/μs

* Higher dV/dt selections available

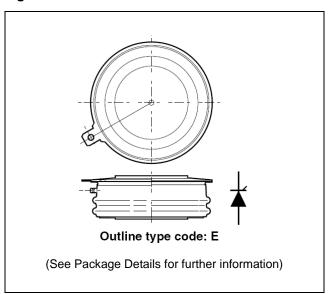


Fig. 1 Package outline



CURRENT RATINGS

$T_{case} = 60$ °C unless stated otherwise

Symbol	Parameter	Test Conditions		Units
Double Si	Double Side Cooled			
I _{T(AV)}	Mean on-state current	Half wave resistive load	470	А
I _{T(RMS)}	RMS value	-	740	А
Ι _Τ	Continuous (direct) on-state current	-	660	А

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{TSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 125$ °C	6.3	kA
l ² t	I ² t for fusing	$V_R = 0$	0.198	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.041	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Double side cooled	DC	-	0.01	°C/W
T _{vj}	Virtual junction temperature	Blocking V _{DRM} / _{VRRM}		-	125	°C
T _{stg}	Storage temperature range			-40	140	°C
F _m	Clamping force			4	6	kN



DYNAMIC CHARACTERISTICS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
I _{RRM} /I _{DRM}	Peak reverse and off-state current	At V _{RRM} /V _{DRM} , T _{case} = 125°C		-	30	mA
dV/dt	Max. linear rate of rise of off-state voltage	To 67% V _{DRM} , T _j = 125°C, ga	To 67% V _{DRM} , T _j = 125°C, gate open		-	V/µs
dl/dt	Rate of rise of on-state current	From 67% V _{DRM} to1000A	Repetitive 50Hz	-	150	A/µs
		Gate source 30V, 10Ω,	Non-repetitive	-	1000	A/µs
		$t_r < 0.5 \mu s, T_j = 125 ^{\circ} C$				
V _T	On-state voltage	IT = 1500A, T _{case} = 125°C			3.63	V
V _{T(TO)}	Threshold voltage	T _{case} = 125°C		-	1.04	V
r _T	On-state slope resistance	T _{case} = 125°C		-	1.725	mΩ
t _{gd}	Delay time	$V_D = 67\% \ V_{DRM}$, gate source 30V, 10Ω		-	3.0	μs
	·	$t_r = 0.5 \mu s, T_j = 25^{\circ}C$				
IL	Latching current	T _j = 25°C,		-	1	Α
lн	Holding current	T _j = 25°C,		-	200	mA

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
V_{GT}	Gate trigger voltage	V _{DRM} = 5V, T _{case} = 25°C	3	V
V_{GD}	Gate non-trigger voltage	At 40% V _{DRM} , T _{case} = 125°C	0.3	V
I _{GT}	Gate trigger current	V _{DRM} = 5V, T _{case} = 25°C	300	mA
I _{GD}	Gate non-trigger current	At 40% V _{DRM} , T _{case} = 125°C	20	mA

CURVES



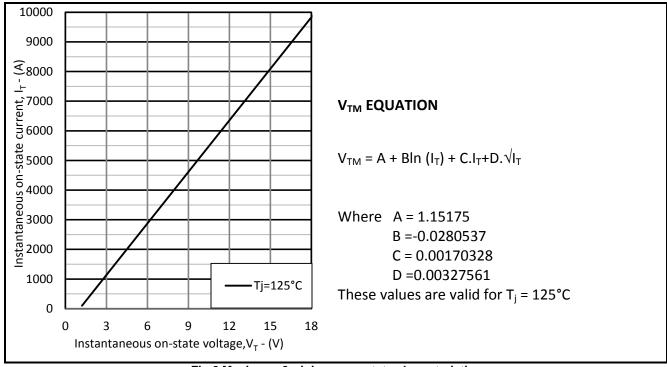


Fig.2 Maximum &minimum on-state characteristics

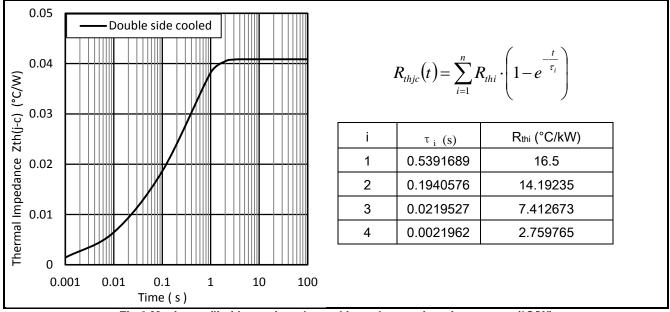
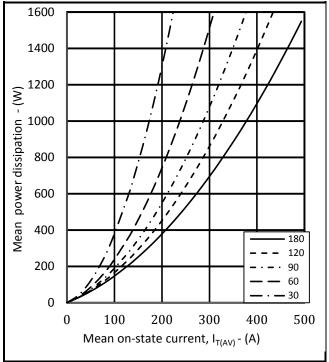
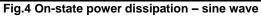


Fig.3 Maximum (limit) transient thermal impedance – junction to case (°C/W)







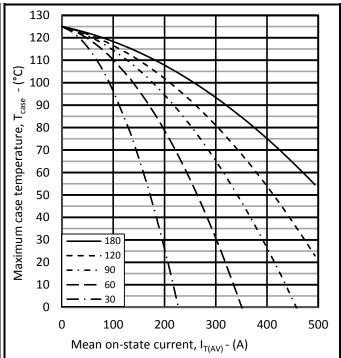


Fig.5 Maximum permissible case temperature, double side cooled – sine wave

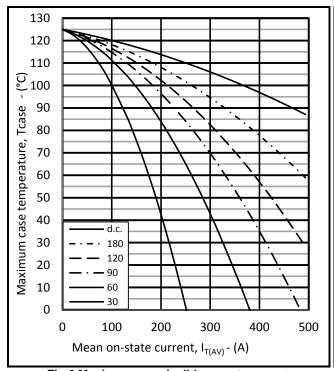


Fig.6 Maximum permissible case temperature, double side cooled – rectangular wave

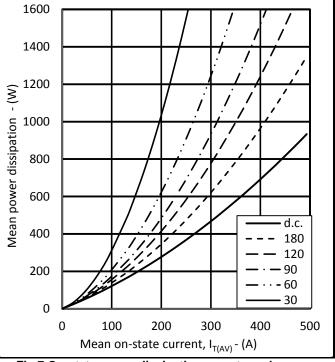
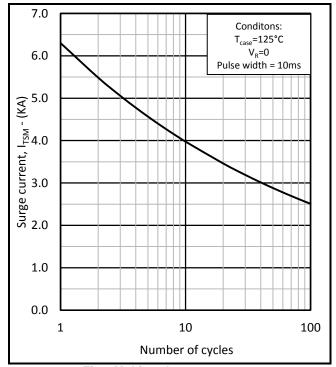


Fig.7 On-state power dissipation - rectangular wave





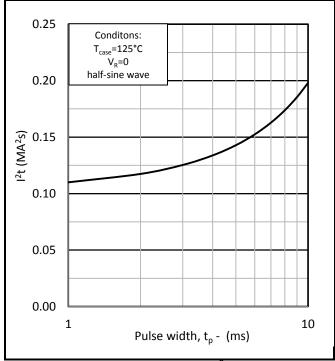


Fig.8 Multi-cycle surge current

Fig.9 Single-cycle I²t

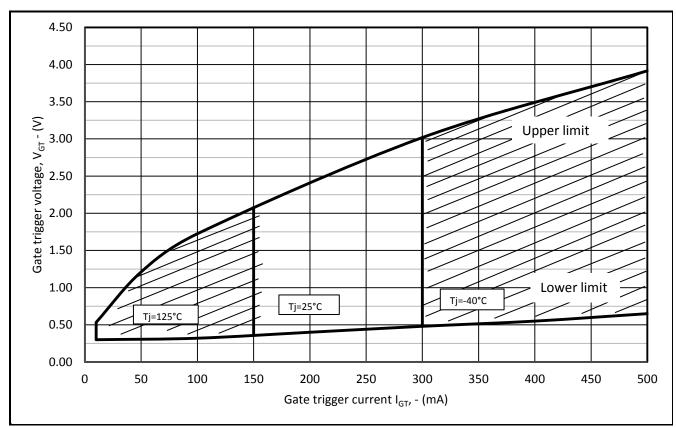


Fig.10 Gate characteristics



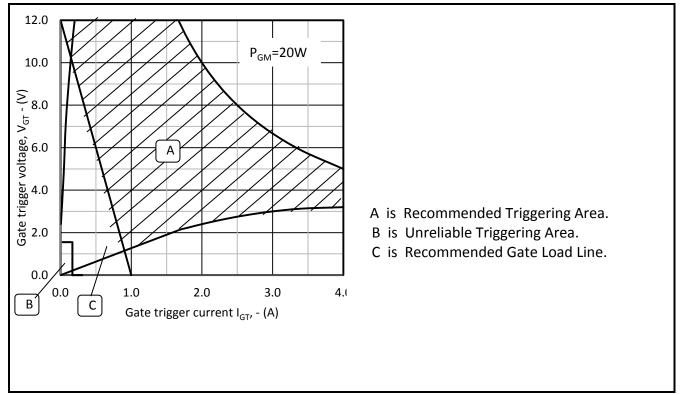


Fig.11 Gate characteristics



PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.

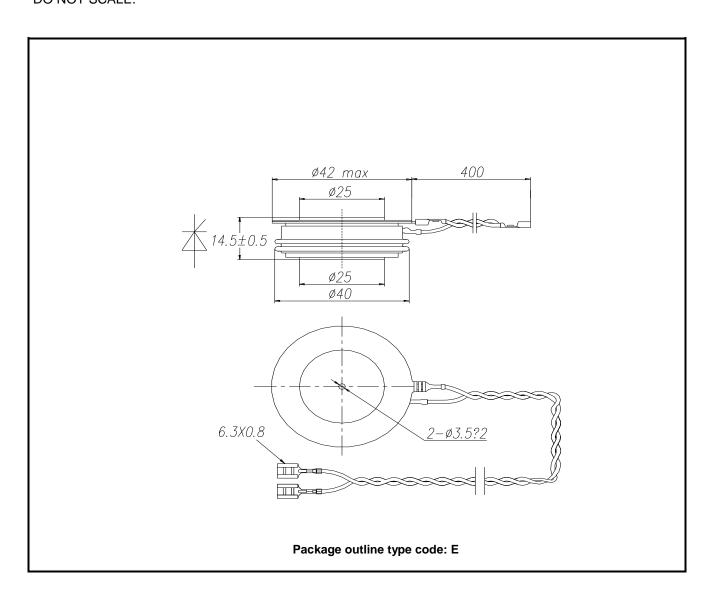


Fig.12 Package outline



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No actual design work on the product has been started.

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The product design is complete and final characterisation for volume production is in progress. The datasheet represents the product as it is now understood but details may change.

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